

Schottky Rectifier, 175 A



PowerTab[®]

PRODUCT SUMMARY				
Package	PowerTab [®]			
I _{F(AV)}	175 A			
V_{R}	30 V			
V _F at I _F	0.52 V			
I _{RM}	650 mA at 125 °C			
T _J max.	125 °C			
Diode variation	Single die			
E _{AS}	80 mJ			

FEATURES

- 150 °C max. operating junction temperature
- High frequency operation
- Ultralow forward voltage drop
- Continuous high current operation
- Guard ring for enhanced ruggedness and long term reliability



COMPLIANT

- Screw mounting only
- Designed and qualified according to JEDEC-JESD47
- PowerTab[®] package
- Compliant to RoHS Directive 2002/95/EC

DESCRIPTION

The VS-175BGQ030 Schottky rectifier has been optimized for ultralow forward voltage drop specifically for low voltage output in high current AC/DC power supplies.

The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
1	Rectangular waveform	175	А		
I _{F(AV)}	T _C	112	°C		
V _{RRM}		30	V		
I _{FSM}	t _p = 5 μs sine	7400	A		
V	175 A _{pk} (typical)	0.47	V		
V_{F}	T_J	150	°C		
T _J	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-175BGQ030	UNITS	
Maximum DC reverse voltage	V _R	30	V	
Maximum working peak reverse voltage	V _{RWM}	30	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _C = 112 °C, rectangular waveform		175	А
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	7400	
	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	1400	A	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 12 A, L = 1.12 mH		80	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		12	А



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS
Forward voltage drop	V _{FM} ⁽¹⁾	100 A	T _J = 25 °C	0.47	0.49	- V
		175 A		0.55	0.59	
		100 A	T _J = 150 °C	0.36	0.39	
		175 A		0.47	0.52	
Reverse leakage current	I _{RM} ⁽¹⁾	T _J = 125 °C, V _R = 15 V		160	220	- mA
		$T_J = 150 ^{\circ}\text{C}, V_R = 30 ^{\circ}\text{V}$		1400	2000	
		T _J = 25 °C	V _R = Rated V _R	1.3	4.5	IIIA
		T _J = 125 °C		450	650	
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$, (test signal range 100 kHz to 1 MHz), 25 °C		85	00	pF
Typical series inductance	L _S	Measured from tab to mounting plane		3	.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		000	V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MEG	THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	PARAMETER SYMBOL TEST CONDITIONS		VALUES	UNITS		
Maximum junction and temperature range	storage	T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resis junction to case	tance,	R _{thJC} DC operation		0.25	°C/W	
Typical thermal resistar case to heatsink	nce,	R _{thCS}	Mounting surface, smooth and greased	0.20	C/VV	
Approximate weight				5	g	
Approximate weight			0.18	OZ.		
Maria Para Laura	minimum			1.2 (10)	N⋅m	
Mounting torque -	maximum			2.4 (20)	(lbf \cdot in)	
Marking device			Case style PowerTab®	175BGQ045		

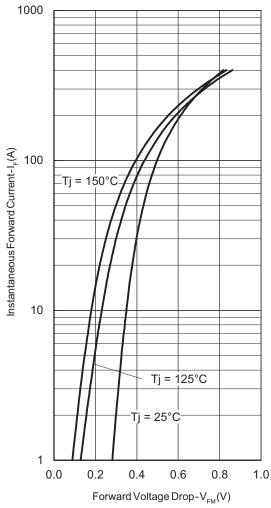


Fig. 1 - Maximum Forward Voltage Drop Characteristics

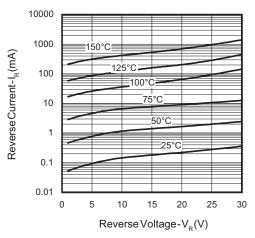


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

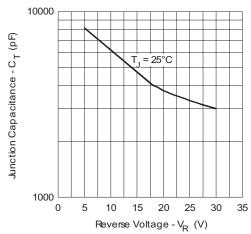


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

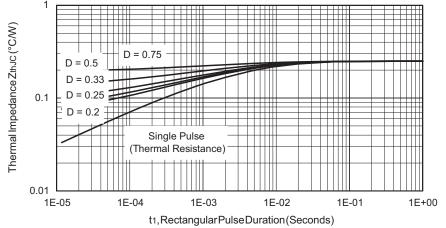


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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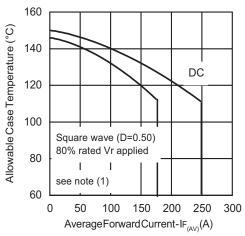


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

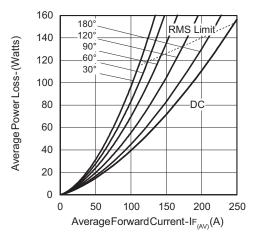


Fig. 6 - Forward Power Loss Characteristics

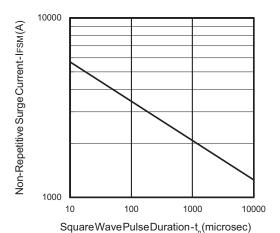


Fig. 7 - Maximum Non-Repetitive Surge Current



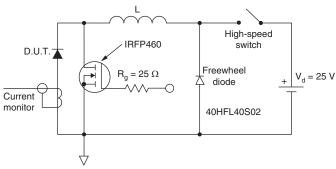


Fig. 8 - Unclamped Inductive Test Circuit

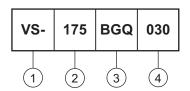
Note

Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = Forward power loss = $I_{F(AV)}$ x V_{FM} at ($I_{F(AV)}$ /D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating

Essential part number

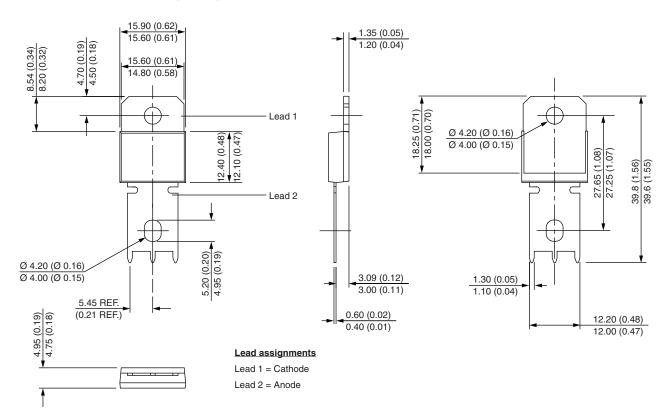
Voltage code = V_{RRM}

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95240</u>				
Part marking information	www.vishay.com/doc?95370			
SPICE model	www.vishay.com/doc?95427			
Application note	www.vishay.com/doc?95179			



PowerTab[®]

DIMENSIONS in millimeters (inches)





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VS-175BGQ030